

ABSTRACT

A first group of data drivers is connected to a sub-field processor, a first power recovery circuit, and a PDP, and a second group of data drivers is connected to a sub-field processor, a second power recovery circuit, and a PDP. The first and second groups of data drivers apply to the PDP data pulses that differ in phases. The first and second power recovery circuits generate a voltage for generating the data pulses to the first and second groups of data drivers due to LC resonance, and discharge and recover charges to and from the PDP. Recovery potentials of recovery capacitors in the first and second power recovery circuits are changed depending on the number of times of switching between discharges and non-discharges of discharge cells in the PDP.